

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Re Application of

Toyonori KANETAKA, et al.

Serial No.: 10/009,750

Filed: December 12, 2001

For: METHOD OF MANUFACTURING CHIP INDUCTOR

: Customer Number: 20277

: Confirmation Number: 1263

: Group Art Unit: 1712

: Examiner: Not Yet Assigned

FOURTH REQUEST FOR CORRECTED FILING RECEIPT

Mail Stop OFR  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application. It is noted that the third entry of the Foreign Priority information is incorrect. Attached is a copy of the Declaration and Power of Attorney Document, which evidences that this information should read: JAPAN 2000-110263 04/12/2000, JAPAN 2000-110264 04/12/2000, JAPAN 2000-110265 04/12/2000, JAPAN 2000-110266 04/12/2000. It is requested that a corrected filing receipt be issued.

Respectfully submitted,

MCDERMOTT WILL & EMERY LLP

  
Michael E. Fogarty  
Registration No. 36,139

600 13<sup>th</sup> Street, N.W.  
Washington, DC 20005-3096  
202.756.8000 MEF:aph  
Facsimile: 202.756.8087  
Date: August 31, 2004



AUG 31 2004  
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COMMISSIONER FOR PATENTS  
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 WASHINGTON, D.C. 20231  
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/009,750	04/30/2002	1712	1308	43890-555	16	36	1

20277  
 McDERMOTT WILL & EMERY"  
 600 13TH STREET, N.W.  
 WASHINGTON, DC 20005-3096

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JUN 14 2002

McDermott, Will & Emery

CONFIRMATION NO. 1263

REPLACEMENT FILING RECEIPT



\*OC000000008261944\*

Date Mailed: 06/11/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

#### Applicant(s)

Toyonori Kanetaka, Hyogo, JAPAN;  
 Toshihiro Yoshizawa, Osaka, JAPAN;  
 Akira Fujimori, Hyogo, JAPAN;  
 Hideaki Nakayama, Hyogo, JAPAN;  
 Hiromasa Yamamoto, Osaka, JAPAN;  
 Mikio Taoka, Osaka, JAPAN;  
 Kenichi Yamada, Hyogo, JAPAN;

#### Domestic Priority data as claimed by applicant

THIS APPLICATION IS A 371 OF PCT/JP01/03149 04/12/2001

#### Foreign Applications

JAPAN 2000-110263 04/12/2000  
 JAPAN 2000-110264 04/12/2000  
~~JAPAN 2000-100165 04/12/2000~~  
 JAPAN 2000-110266 04/12/2000

JAPAN 2000-110265 4/12/2000

Projected Publication Date: To Be Determined - pending completion of Security Review

Non-Publication Request: No

Early Publication Request: No

Title

Method of manufacturing chip inductor

Preliminary Class

438

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Title 35, United States Code, Section 184  
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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P24986

**COMBINED DECLARATION FOR PATENT APPLICATION AND POWER OF ATTORNEY**

(Includes Reference to PCT International Application(s))

Attorney's Docket Number  
43890-555

As below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

METHOD OF MANUFACTURING CHIP INDUCTOR



the specification of which:

☐ is attached hereto.☒ was filed as United States application Serial No. 10/009,750on December 12, 2001

and was amended on \_\_\_\_\_ (if applicable).

☒ was filed as PCT international application Number PCT/JP01/03149on April 12, 2001

and was amended under PCT Article 19 on \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is known to me to be material to patentability in accordance with Title 37, Code of Federal Regulations, §1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, §119(a)-(d) or Section 365(b) of any foreign and/or international application(s) for patent or inventor's certificate or Section 365(a) of any PCT international application(s) designating at least one country other than the United States of America listed below and have also identified below any foreign application(s) for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America filed by me on the same subject matter having a filing date before that of the application(s) of which priority is claimed:

**PRIOR FOREIGN/PCT APPLICATION(S) AND ANY PRIORITY CLAIMS UNDER 35 U.S.C. 119:**

COUNTRY (If PCT, indicate "PCT")	APPLICATION NUMBER	DATE OF FILING (day, month, year)	PRIORITY CLAIMED UNDER 35 USC 119
PCT	PCT/JP01/03149	April 12, 2001	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No
JAPAN	2000-110263	April 12, 2000	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No
JAPAN	2000-110264	April 12, 2000	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No
JAPAN	2000-110265	April 12, 2000	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No
JAPAN	2000-110266	April 12, 2000	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No

I hereby claim the benefit under 35 USC §119(e) of any United States provisional application(s) listed below.

**PRIOR PROVISIONAL APPLICATION(S):**

Application Number	Filing Date

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s), or §365(c) of any PCT international application(s) designating the United States of America that is/are listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in that/those prior application(s) in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1.56 which occurred between the filing date of the prior application(s) and the national or PCT international filing date of this application.

**PRIOR U.S. APPLICATIONS OR PCT INTERNATIONAL APPLICATIONS DESIGNATING THE U.S. FOR BENEFIT UNDER 35 U.S.C. 120:**

U.S. APPLICATIONS			STATUS (Check One)		
U.S. Application Number	U.S. Filing Date		Patented	Pending	Abandoned
PCT APPLICATIONS DESIGNATING THE U.S.					
PCT Application No.	PCT Filing Date	U.S. Serial Numbers Assigned (if any)			
PCT/JP01/03149	April 12, 2001				

**POWER OF ATTORNEY:** As named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith: Stephen A. Becker, Reg. No. 26,527; John G. Bisbikis, Reg. No. 37,095; Daniel Bucca, Reg. No. 42,368; Kenneth L. Cage, Reg. No. 26,151; Jennifer Chen, Reg. No. 42,404; Bernard P. Codd, Reg. No. 46,429; Lawrence T. Cullen, Reg. No. 44,489; Paul Devinsky, Reg. No. 28,553; Margaret M. Duncan, Reg. No. 30,879; Shamita De. Etienne-Cummings, Reg. No. 46,072; Ramyar M. Farid, Reg. No. 46,692; Brian E. Ferguson, Reg. No. 36,801; Michael E. Fogarty, Reg. No. 36,139; John R. Fuisz, Reg. No. 37,327; Willem F. Gadiano, Reg. No. 37,136; Keith E. George, Reg. No. 34,111; Matthew V. Grumblin, Reg. No. 44,427; John A. Hankins, Reg. No. 32,029; Eric J. Kraus, Reg. No. 36,190; Catherine Krupka, Reg. No. 46,227; Jack Q. Lever, Reg. No. 28,149; Raphael V. Lupo, Reg. No. 28,363; Burman Y. Mathis III, Reg. No. 44,907; Michael A. Messina, Reg. No. 33,424; Dawn L. Palmer, Reg. No. 41,238; Joseph H. Paquin, Jr., Reg. No. 31,647; Scott D. Paul, Reg. No. 42,984; William D. Pegg, Reg. No. 42,988; Robert L. Price, Reg. No. 22,685; Gene Z. Robinson, Reg. No. 33,351; Joy Ann G. Serauskas, Reg. No. 27,952; David A. Spenard, Reg. No. 37,449; Arthur J. Steiner, Reg. No. 26,106; David L. Stewart, Reg. No. 37,578; Wesley Strickland, Reg. No. 44,363; Michael D. Switzer, Reg. No. 39,552; David M. Tennant, Reg. No. 48,362; Daniel S. Trainor, Reg. No. 43,959; Kelli N. Watson, Reg. No. 47,170; Cameron K. Weiffenbach, Reg. No. 44,488; Aaron Weisstuch, Reg. No. 41,557; Edward J. Wise, Reg. No. 34,523; Jeffrey A. Woller, Reg. No. 48,041; Alexander V. Yampolsky, Reg. No. 36,324; and Robert W. Zelnick, Reg. No. 36,976, all of McDermott, Will & Emery.

**Send Correspondence to:**

McDERMOTT, WILL & EMERY  
600 13<sup>th</sup> Street, N.W.  
Washington, D.C. 20005-3096

**Direct Telephone Calls to:**

(name and telephone number)

(202) 756-8000

201	Full Name of Inventor	Family Name	First Given Name	Second Given Name
	Toyonori KANETAKA	KANETAKA	Toyonori	
	Residence and Citizenship	City	State or Foreign Country	Country of Citizenship
	Japanese	Hyogo	JAPAN	JAPAN
202	Post Office Address	Post Office Address	City	State & Zip Code/Country
	494-10, Kokonokaichikamino-cho	Toyooka-shi	Hyogo	668-0051 JAPAN
	Full Name of Inventor	Family Name	First Given Name	Second Given Name
	Toshihiro YOSHIZAWA	YOSHIZAWA	Toshihiro	
203	Residence and Citizenship	City	State or Foreign Country	Country of Citizenship
	Japanese	Osaka	JAPAN	JAPAN
	Post Office Address	Post Office Address	City	State & Zip Code/Country
	1-6-32, Omido	Higashiosaka-shi	Osaka	577-0817 JAPAN
203	Full Name of Inventor	Family Name	First Given Name	Second Given Name
	Akira FUJIMORI	FUJIMORI	Akira	
	Residence and Citizenship	City	State or Foreign Country	Country of Citizenship
	Japanese	Hyogo	JAPAN	JAPAN
203	Post Office Address	Post Office Address	City	State & Zip Code/Country
	5-37, Sakuramachi	Toyooka-shi	Hyogo	668-0043 JAPAN

204	Full Name of Inventor	Family Name	First Given Name	Second Given Name
	Hideaki NAKAYAMA	NAKAYAMA	Hideaki	
	Residence and Citizenship	City	State or Foreign Country	Country of Citizenship
	Japanese	Hyogo	JAPAN	JAPAN
	Post Office Address	Post Office Address	City	State & Zip Code/Country
	274-1-305, Machibun	Izushi-cho, Izushi-gun	Hyogo	668-0221 JAPAN
205	Full Name of Inventor	Family Name	First Given Name	Second Given Name
	Hiromasa YAMAMOTO	YAMAMOTO	Hiromasa	
	Residence and Citizenship	City	State or Foreign Country	Country of Citizenship
	Japanese	Osaka	JAPAN	JAPAN
	Post Office Address	Post Office Address	City	State & Zip Code/Country
	C3-203, 3-3, Shinsenriminamimachi	Toyonaka-shi	Osaka	560-0084 JAPAN
206	Full Name of Inventor	Family Name	First Given Name	Second Given Name
	Mikio TAOKA	TAOKA	Mikio	
	Residence and Citizenship	City	State or Foreign Country	Country of Citizenship
	Japanese	Osaka	JAPAN	JAPAN
	Post Office Address	Post Office Address	City	State & Zip Code/Country
	6-12-21, Takayanagi	Neyagawa-shi	Osaka	572-0051 APAN
207	Full Name of Inventor	Family Name	First Given Name	Second Given Name
	Kenichi YAMADA	YAMADA	Kenichi	
	Residence and Citizenship	City	State or Foreign Country	Country of Citizenship
	Japanese	Hyogo	JAPAN	JAPAN
	Post Office Address	Post Office Address	City	State & Zip Code/Country
	257-1-202, Machibun	Izushi-cho, Izushi-gun	Hyogo	668-0221 JAPAN

I hereby declare that all statement made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Signature of Inventor 201:	Signature of Inventor 202:	Signature of Inventor 203:
<i>Toyonori Karotaka</i>	<i>T. Yoshigawa</i>	<i>Akira Fujimori</i>
Date April 4, 2002	Date April 4, 2002	Date April 4, 2002
Signature of Inventor 204	Signature of Inventor 205	Signature of Inventor 206
<i>Hideaki Nakayama</i>	<i>Hiromasa Yamamoto</i>	<i>Mikio Taoka</i>
Date April 4, 2002	Date april 4, 2002	Date April 4, 2002
Signature of Inventor 207		
<i>Kenichi Yamada</i>		
Date April 4, 2002		